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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO.

09/737, 218 12/14/00 NORMAN

TM21/0917

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DATEMALED:

09/17/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No. 09/737,218

Applicant(s)

Norman et al.

Examiner

B. James Pelkari

Art Unit 2186



 The MAILING DATE of this communication appear 	ars on the cover sheet with the correspondence address –
Period for Reply	
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.	
- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed	
after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will	
be considered timely. - If NO period for reply is specified above, the maximum statutory per	iod will apply and will expire SIX (6) MONTHS from the mailing date of this
 communication. Failure to reply within the set or extended period for reply will, by sta Any reply received by the Office later than three months after the management patent term adjustment. See 37 CFR 1:704(b). 	tute, cause the application to become ABANDONED (35 U.S.C. § 133). ailing date of this communication, even if timely filed, may reduce any
Status	
1) X Responsive to communication(s) filed on <u>Dec 14</u>	1, 2000
,—	action is non-final.
3) Since this application is in condition for allowance closed in accordance with the practice under Ex	e except for formal matters, prosecution as to the merits is parte Quayle35 C.D. 11; 453 O.G. 213.
Disposition of Claims	
4) X Claim(s) <u>1-3, 25, 47, 55-59, and 61</u>	is/are pending in the applica
	is/are withdrawn from considera
5) Claim(s)	is/are allowed.
6)	is/are rejected.
7) Claim(s)	is/are objected to.
8) X Claims <u>1-3, 25, 47, 55-59 and 61</u>	are subject to restriction and/or election requirem
Application Papers	
9) The specification is objected to by the Examiner.	
10) The drawing(s) filed on	s/are objected to by the Examiner.
11) ☐ The proposed drawing correction filed on	is: a反 approved b) ☐disapproved.
12) The oath or declaration is objected to by the Exam	niner.
Priority under 35 U.S.C. § 119	
13) Acknowledgement is made of a claim for foreign p	priority under 35 U.S.C. § 119(a)-(d).
a) ☐ All b) ☐ Some* c) ☐None of:	
1. Certified copies of the priority documents ha	ve been received.
 Certified copies of the priority documents ha Certified copies of the priority documents ha 	1
 2. Certified copies of the priority documents ha 3. Copies of the certified copies of the priority of application from the International Bure 	ve been received in Application No locuments have been received in this National Stage are (PCT Rule 17.2(a)).
2. Certified copies of the priority documents ha3. Copies of the certified copies of the priority of	ve been received in Application No locuments have been received in this National Stage rau (PCT Rule 17.2(a)). The certified copies not received.
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Certified copies of the priority documents ha Copies of the certified copies of the priority of application from the International Bure *See the attached detailed Office action for a list of the standard of the Acknowledgement is made of a claim for domestic Attachment(s)	ve been received in Application No locuments have been received in this National Stage au (PCT Rule 17.2(a)). ne certified copies not received. c priority under 35 U.S.C. § 119(e).

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DETAILED ACTION

Election/Restriction

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - Claims 1-3, drawn to formulating addresses of an array of memory cells, classified in class 711, subclass 209.
 - II. Claim 25, drawn to a memory system including a tag bus connected to a decoder and a data bus connected to a comparator, classified in class 710, subclass 126.
 - III. Claims 47 and 55-57, drawn to manipulation of the addresses and address bits, classified in class 711, subclass 211.
 - IV. Claims 58-59 and 61, drawn to formulating addresses of an array of memory cells under the protection of a lockout circuit and select signal I/O, classified in class 711, subclass 152.
- 2. The inventions are distinct, each from the other because of the following reasons: Inventions I and II are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because there is no

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requirement for a tag bus connected to a decoder and a data bus connected to a comparator. The subcombination has separate utility such as performing address comparisons using an address comparator.

- 3. Inventions I and III are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention III has separate utility such as manipulation of addresses in three-bit portions. See MPEP § 806.05(d).
- 4. Inventions I and IV are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because there is no requirement for lockout circuitry or a select signal to control such circuitry. The subcombination has separate utility such as the ability to lockout cells of the memory array.
- 5. Inventions II and III are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately

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usable. In the instant case, invention III has separate utility such as the manipulation of addresses in three-bit portions. See MPEP § 806.05(d).

- 6. Inventions II and IV are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention IV has separate utility such as the ability to lockout access to cells of the memory array. See MPEP § 806.05(d).
- 7. Inventions III and IV are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention IV has separate utility such as the ability to lockout access to cells of the memory array. See MPEP § 806.05(d).
- 8. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.
- 9. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

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10. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the

inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently

named inventors is no longer an inventor of at least one claim remaining in the application. Any

amendment of inventorship must be accompanied by a petition under 37 CFR 1.48(b) and by the

fee required under 37 CFR 1.17(i).

11. Any inquiry concerning this communication or earlier communications from the examiner

should be directed to James Peikari whose telephone number is (703) 305-3824.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor,

Matthew Kim, can be reached at (703) 305-3821.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the Group receptionist whose telephone number is (703) 305-3900.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 308-9051 (for formal communications intended for entry)

or:

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(703) 305-9731 (for informal or draft communications, please label

"PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington. VA., Sixth Floor (Receptionist).

B. James Peikari Primary Examiner Art Unit 2186

September 15, 2001